

878 Family, High Density Input/Output



878 in 12 Bay Chassis

Model Selection Guide:

878: Family of High Density Input/Output Devices		
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878 Family Order Guides

Model: 878

878, High Density Input/Output Device
Category and Description of Choices (X is Default)

Base Model

878, DIOD, C07A5 Chassis

Bay1, PowerSupply

V10, 20-300 V dc/55-285 V ac

Bay2, cPCI Slot

C00, Blank Cover

P30A, Digital I/O, 8 point set for 100 V dc

P30A, Digital I/O, 8 point set for 300 V dc

P33, Eight Digital Outputs

P40, Transducer Module, 8 inputs set for 0-10 V dc

P40, Transducer Module, 8 inputs set for 4-20 mA

P40, Transducer Module, 8 inputs set for 0-1 mA

None, covered by P31 in next slot

Bay3, cPCI Slot

C00, Blank Cover

P30A, Digital I/O, 8 point set for 100 V dc

P31, Digital I/O, 16 point set for 100 V dc

P30A, Digital I/O, 8 point set for 300 V dc

P31, Digital I/O, 16 point set for 300 V dc

P33, Eight Digital Outputs

P40, Transducer Module, 8 inputs set for 0-10 V dc

P40, Transducer Module, 8 inputs set for 4-20 mA

P40, Transducer Module, 8 inputs set for 0-1 mA

None, covered by P31 in next slot

Bay4, cPCI Slot

C00, Blank Cover

P30A, Digital I/O, 8 point set for 100 V dc

P31, Digital I/O, 16 point set for 100 V dc

P30A, Digital I/O, 8 point set for 300 V dc

P31, Digital I/O, 16 point set for 300 V dc

P33, Eight Digital Outputs

P40, Transducer Module, 8 inputs set for 0-10 V dc

P40, Transducer Module, 8 inputs set for 4-20 mA

P40, Transducer Module, 8 inputs set for 0-1 mA

Open for H12 with Ethernet option selected in next slot

Bay5, cPCI Slot, Host

H12M256, Host Processor, 256 MB

H12M256E1, Host Processor, 256 MB, with E1 Ethernet (RJ45 10/100Mb)

H12M256E3, Host Processor, 256 MB, with E3 Ethernet (RJ45 10/100Mb + 100 Fiber)

Bay6, cPCI Slot, DSP

None, covered by host processor

Bay7, Signal Input

C01, blank cover

Firmware Level

X, Latest Firmware Version

[End of Order Guide](#)

Build Order#

Position 1-3

878

Position 4

2

Position 5

0

6

8

A

U

V

W

Y

Position 6

0

6

7

8

9

A

U

V

W

Y

Position 7

0

6

7

8

9

A

U

V

W

Y

Position 8

8

B

E

Position 9

Y

Position 10

0

Position 11

X

878 Family Order Guides

Model: 878

878, High Density Input/Output Device in Intermediate Chassis

Category and Description of Choices (X is Default)

Base Model

878, DIOD, C10A7 Chassis

Bay1, PowerSupply

V10, 20-300 V dc/55-285 V ac

Bay2, cPCI Slot

C00, Blank Cover

P30A, Digital I/O, 8 point set for 100 V dc

P30A, Digital I/O, 8 point set for 300 V dc

P33, Eight Digital Outputs

P40, Transducer Module, 8 inputs set for 0-10 V dc

P40, Transducer Module, 8 inputs set for 4-20 mA

P40, Transducer Module, 8 inputs set for 0-1 mA

None, covered by P31 in next slot

Bay3, cPCI Slot

C00, Blank Cover

P30A, Digital I/O, 8 point set for 100 V dc

P31, Digital I/O, 16 point set for 100 V dc

P30A, Digital I/O, 8 point set for 300 V dc

P31, Digital I/O, 16 point set for 300 V dc

P33, Eight Digital Outputs

P40, Transducer Module, 8 inputs set for 0-10 V dc

P40, Transducer Module, 8 inputs set for 4-20 mA

P40, Transducer Module, 8 inputs set for 0-1 mA

None, covered by P31 in next slot

Bay4, cPCI Slot

C00, Blank Cover

P30A, Digital I/O, 8 point set for 100 V dc

P31, Digital I/O, 16 point set for 100 V dc

P30A, Digital I/O, 8 point set for 300 V dc

P31, Digital I/O, 16 point set for 300 V dc

P33, Eight Digital Outputs

P40, Transducer Module, 8 inputs set for 0-10 V dc

P40, Transducer Module, 8 inputs set for 4-20 mA

P40, Transducer Module, 8 inputs set for 0-1 mA

None, covered by P31 in next slot

Bay5, cPCI Slot

C00, Blank Cover

P30A, Digital I/O, 8 point set for 100 V dc

P31, Digital I/O, 16 point set for 100 V dc

P30A, Digital I/O, 8 point set for 300 V dc

P31, Digital I/O, 16 point set for 300 V dc

P33, Eight Digital Outputs

P40, Transducer Module, 8 inputs set for 0-10 V dc

P40, Transducer Module, 8 inputs set for 4-20 mA

P40, Transducer Module, 8 inputs set for 0-1 mA

None, covered by P31 in next slot

Bay6, cPCI Slot

C00, Blank Cover

P30A, Digital I/O, 8 point set for 100 V dc

P31, Digital I/O, 16 point set for 100 V dc

P30A, Digital I/O, 8 point set for 300 V dc

P31, Digital I/O, 16 point set for 300 V dc

P33, Eight Digital Outputs

P40, Transducer Module, 8 inputs set for 0-10 V dc

P40, Transducer Module, 8 inputs set for 4-20 mA

P40, Transducer Module, 8 inputs set for 0-1 mA

Open for H12 with Ethernet option selected in next slot

Build Order#

Position 1-3

878

Position 4

2

Position 5

0

6

8

A

U

V

W

Y

Position 6

0

6

7

8

9

A

U

V

W

Y

Position 7

0

6

7

8

9

A

U

V

W

Y

Position 8

0

6

7

8

9

A

U

V

W

Y

Position 9

0

6

7

8

9

A

U

V

W

Y

Order Guide continues on next page



878 Family Order Guides

Model: 878

878, High Density Input/Output Device in Intermediate Chassis

Category and Description of Choices (X is Default)

Bay7, cPCI Slot, Host

H12M256, Host Processor, 256 MB

H12M256E1, Host Processor, 256 MB, with E1 Ethernet (RJ45 10/100Mb)

H12M256E3, Host Processor, 256 MB, with E3 Ethernet (RJ45 10/100Mb + 100 Fiber)

Bay8, cPCI Slot, DSP

None, covered by host processor

Bay9

C00, Blank Cover

Bay10, Signal Input

C01, blank cover

Firmware Level

X, Latest Firmware Version

[End of Order Guide](#)

Build Order#

Position 10

8

B

E

Position 11

Y

Position 12

0

Position 13

0

Position 14

X

878 Family Order Guides

Model: 878

878, High Density Input/Output Device in Large Chassis

Category and Description of Choices (X is Default)

Base Model

878, DIOD, C12A8 Chassis

Bay1, PowerSupply

V10, 20-300 V dc/55-285 V ac plus blank slot cover

V10NC, 20-300 V dc/55-285 V ac, cover by P31 next Slot

Bay2, cPCI Slot

C00, Blank Cover

P30A, Digital I/O, 8 point set for 100 V dc

P31, Digital I/O, 16 point set for 100 V dc

P30A, Digital I/O, 8 point set for 300 V dc

P31, Digital I/O, 16 point set for 300 V dc

P33, Eight Digital Outputs

P40, Transducer Module, 8 inputs set for 0-10 V dc

P40, Transducer Module, 8 inputs set for 4-20 mA

P40, Transducer Module, 8 inputs set for 0-1 mA

None, covered by P31 in next slot

Bay3, cPCI Slot

C00, Blank Cover

P30A, Digital I/O, 8 point set for 100 V dc

P31, Digital I/O, 16 point set for 100 V dc

P30A, Digital I/O, 8 point set for 300 V dc

P31, Digital I/O, 16 point set for 300 V dc

P33, Eight Digital Outputs

P40, Transducer Module, 8 inputs set for 0-10 V dc

P40, Transducer Module, 8 inputs set for 4-20 mA

P40, Transducer Module, 8 inputs set for 0-1 mA

None, covered by P31 in next slot

Bay4, cPCI Slot

C00, Blank Cover

P30A, Digital I/O, 8 point set for 100 V dc

P31, Digital I/O, 16 point set for 100 V dc

P30A, Digital I/O, 8 point set for 300 V dc

P31, Digital I/O, 16 point set for 300 V dc

P33, Eight Digital Outputs

P40, Transducer Module, 8 inputs set for 0-10 V dc

P40, Transducer Module, 8 inputs set for 4-20 mA

P40, Transducer Module, 8 inputs set for 0-1 mA

None, covered by P31 in next slot

Bay5, cPCI Slot

C00, Blank Cover

P30A, Digital I/O, 8 point set for 100 V dc

P31, Digital I/O, 16 point set for 100 V dc

P30A, Digital I/O, 8 point set for 300 V dc

P31, Digital I/O, 16 point set for 300 V dc

P33, Eight Digital Outputs

P40, Transducer Module, 8 inputs set for 0-10 V dc

P40, Transducer Module, 8 inputs set for 4-20 mA

P40, Transducer Module, 8 inputs set for 0-1 mA

None, covered by P31 in next slot

Build Order#

Position 1-3

878

Position 4

3

4

Position 5

0

6

7

8

9

A

U

V

W

Y

Position 6

0

6

7

8

9

A

U

V

W

Y

Position 7

0

6

7

8

9

A

U

V

W

Y

Position 8

0

6

7

8

9

A

U

V

W

Y

Order Guide continues on next page

878 Family Order Guides

Model: **878**

878, High Density Input/Output Device in Large Chassis

Category and Description of Choices (X is Default)

Bay6, cPCI Slot

C00, Blank Cover
P30A, Digital I/O, 8 point set for 100 V dc
P31, Digital I/O, 16 point set for 100 V dc
P30A, Digital I/O, 8 point set for 300 V dc
P31, Digital I/O, 16 point set for 300 V dc
P33, Eight Digital Outputs
P40, Transducer Module, 8 inputs set for 0-10 V dc
P40, Transducer Module, 8 inputs set for 4-20 mA
P40, Transducer Module, 8 inputs set for 0-1 mA
None, covered by P31 in next slot

Bay7, cPCI Slot

C00, Blank Cover
P30A, Digital I/O, 8 point set for 100 V dc
P31, Digital I/O, 16 point set for 100 V dc
P30A, Digital I/O, 8 point set for 300 V dc
P31, Digital I/O, 16 point set for 300 V dc
P33, Eight Digital Outputs
P40, Transducer Module, 8 inputs set for 0-10 V dc
P40, Transducer Module, 8 inputs set for 4-20 mA
P40, Transducer Module, 8 inputs set for 0-1 mA
Open for H12 with Ethernet option selected in next slot

Bay8, cPCI, Slot Host

H12M256, Host Processor, 256 MB
H12M256E1, Host Processor, 256 MB, with E1 Ethernet (RJ45 10/100Mb)
H12M256E3, Host Processor, 256 MB, with E3 Ethernet (RJ45 10/100Mb + 100 Fiber)

Bay9, cPCI Slot, DSP

None, covered by host processor

Bay10

C00, Blank Cover

Bay11, Signal Input

C01, blank cover

Firmware Level

X, Latest Firmware Version

[End of Order Guide](#)

Build Order#

Position 9

0
6
7
8
9
A
U
V
W
Y

Position 10

0
6
7
8
9
A
U
V
W
Y

Position 11

8
B
E

Position 12

Y

Position 13

0

Position 14

0

Position 15

X